Amendments to the Specification

Please replace the paragraph starting with "Still referring to Fig. 7" beginning on page 18, lines 13-25 and ending on page 19, lines 1-3 with the following amended paragraph:

Still referring to Fig. 7, a description for the reverse link or MS transmission is given next. The frame generator circuit 730 of the MS 105 disassembles the data input from the MS into a plurality of data and generates a frame. The permutations of the information of the frame that were error corrected and encoded in the encoder 729 are changed in the interleaver 728. The encoding parameters and interleave parameters are supplied at this time to the encoder 729 by the MS radio resource controller 732. The buffer/total speed controller 727 determines the total transmission data rate of all information by adding all the reverse link data rates set by the reverse link data rate controller 724. The reverse link data rate controller 724 determines the data rate for each channel based on the reverse link total FER that is received via the forward link and the FER or the E_b/N_0 of each reverse link. This control may be implemented while taking into account the usage status of all BS radio resources in terms of time_slot, encoding and frequencies of connectable links with the MS. Signals corresponding to the BS are subsequently assigned by the distributor circuit 726. One transmitter is assigned to each BS in a ratio of one to one.